

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2673
Serial No.: 09/504,235 Examiner : D. Lewis
Filed : February 15, 2000
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$240 in payment of the late submission fee of \$1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 4/27/01

Scott C. Harris
Reg. No. 32,030

PTO Customer No. 20985
Fish & Richardson P.C.
4350 La Jolla Village Drive, Suite 500
San Diego, CA 92122
Telephone: (858) 678-5070
Facsimile: (858) 678-5099

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 07977-008004	Application No. 09/504,235
	Applicant Yamazaki, et al.		
	Filing Date February 15, 2000	Group Art Unit 2673	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,206,749	Apr. 27, 1993	Zavacky et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
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	AJ						
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Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
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	AL							
	AM							
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	AO							
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Other Documents (include Author, Title, Date, and Place of Publication)		
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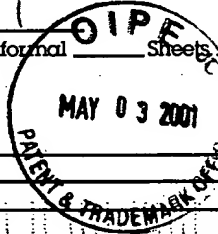
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PATENT

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The Patent and Trademark Office date stamp sets forth the date of receipt of:

Applicant or Patentee Yamazaki, et al.
 No. (Application, Appeal, Interference, Patent, Reexam) 09/504,735
 Filing or Issue Date February 15, 2000
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09/504,735

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Title:

METHOD OF MANUFACTURING A SEMI-CONDUCTOR DEVICE

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